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TO: EXAMINER: VINH P. NGUYEN

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SERIAL NO.: 09/819,287

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:

Johannes P.M. Van LAMMEREN et al.

Group Art Unit: 2829

Serial No.: 09/819,287

Examiner: Vinh P. NGUYEN

Filed: 28 March 2001

METHOD AND APPARATUS FOR
INSPECTING AN INTEGRATED
CIRCUIT BY MEASURING
VOLTAGE ON A SIGNAL LINE

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RESPONSE UNDER 37 C.F.R. § 1.113

JAN 21 2003

Honorable Commissioner for Patents
Washington, D.C. 20231

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Sir:

In response to the Office Action dated 18 October 2002, the period for response to which is 21 January 2003, reexamination and reconsideration of the above-referenced U.S. patent application is respectfully requested in view of the following

Remarks.

REMARKS

The Office Action rejected pending claims 8 and 13 under the judicially-created doctrine of obviousness-type double patenting over claims 1 and 8 of U.S. Patent 6,239,604. Filed herewith is a Terminal Disclaimer under 37 C.F.R. § 1.321.

Accordingly, it is respectfully submitted that the obviousness-type double patenting rejection of claims 8 and 13 has now been overcome.